

# 5 T836/837 Fault Finding

The following test procedures and fault finding flow charts may be used to help locate a hardware problem, however they are by no means a complete fault finding procedure. If the fault still exists after having progressed through them in a logical manner, contact your nearest authorised Tait Dealer or Service Centre. Further assistance may be obtained from the Customer Support Group, Radio Infrastructure Division, Tait Electronics Ltd, Christchurch, New Zealand.

The following topics are covered in this section.

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## 5.1 Visual Checks

Remove the covers from the T836/837 and inspect the PCB for damaged or broken components, paying particular attention to the surface mounted devices (SMD's).

Check for defective solder joints. If repair or replacement is considered necessary, refer to Section 3 of Part A.

## 5.2 Component Checks

If a transistor is suspected of faulty operation, an indication of its performance can be assessed by measuring the forward and reverse resistance of the junctions. First make sure that the transistor is not shunted by some circuit resistance (unless the device is completely desoldered). A 20k ohm/V or better multimeter should be used for taking the measurements, using only the medium or low resistance ranges.

The collector current drawn by multi-junction transistors is a further guide to their performance.

If an IC is suspect, the most reliable check is to measure the DC operating voltages. Due to the catastrophic nature of most IC failures, the pin voltages will usually be markedly different from the recommended values in the presence of a fault. The recommended values can be obtained from either the circuit diagram or the component data catalogue.

## 5.3 DC Checks

### 5.3.1 Power Rails

Refer to the test points and options diagrams (Section 6) for test point locations, and to the regulator fault finding chart (Section 5.5.1) for fault diagnosis.

Check the 13.8V (TP1 in the transmitter) and 9V (TP2 in the synthesiser) supplies at their test points with a DMM.

Check the 5V (TP4 in the synthesiser) and 20V (TP3 in the regulator) rails at their respective test points.

Check that Tx reg. (TP5 in the exciter) comes up to 8.8V when the exciter is keyed.

Check for short circuits.

### 5.3.2 VCO Locking

Key the exciter.

Using a DMM, monitor the VCO control voltage at the junction of L1 and R1 on the VCO PCB.

If the synthesiser is locked and the VCO aligned, the voltage at this point should be between 5 and 15V.

If the VCO is not locked, refer to the synthesiser fault finding chart (Section 5.5.2).

**Note:** When changing frequencies, only the three least significant bits of the EPROM address (e.g. DIP switch) will initiate a synthesiser programme cycle. Changing a high order bit will therefore not result in a change of frequency unless a low order bit is also changed last.

## 5.4 RF Checks

In-circuit RF levels may be measured with an RF probe on which the earth lead has been shortened to a minimum (i.e. 13mm). Refer to the circuit diagrams for typical levels.

### 5.4.1 T836 Drive Power

Refer to the drive amplifier fault finding chart (Section 5.5.3).

Ensure that the VCO locks (refer to Section 5.3.2).

Connect the drive output to a power meter and key the transmitter.

Check that the output power is between 400 and 700mW.

**Note:** The lock detector (synthesiser, IC5 pin 3) will not allow the RF signal to reach the PA if the synthesiser is out of lock by switching the supply to the exciter amplifier (Q300, Q301).

### 5.4.2 T836 PA Output Power

Reconnect the drive output to the PA input.

Connect the PA to a power meter and key the transmitter.

Check that the output power is >30W with power control &RV356 adjusted fully clockwise.

### 5.4.3 T837 Output Power

Refer to the exciter drive amplifier fault finding chart (Section 5.5.5).

Ensure that the VCO locks (refer to Section 5.3.2).

Connect the exciter output to a power meter and key the exciter.

Check that the output power is between 700 and 1000mW.

**Note:** The lock detector (synthesiser, IC5 pin 3) will not allow the RF signal to reach the drive amplifier if the synthesiser is out of lock by switching the supply to the exciter amplifier (Q300, Q301).

## 5.4.4 Audio And Modulation

Refer to the audio processor fault finding chart (Section 5.5.6).

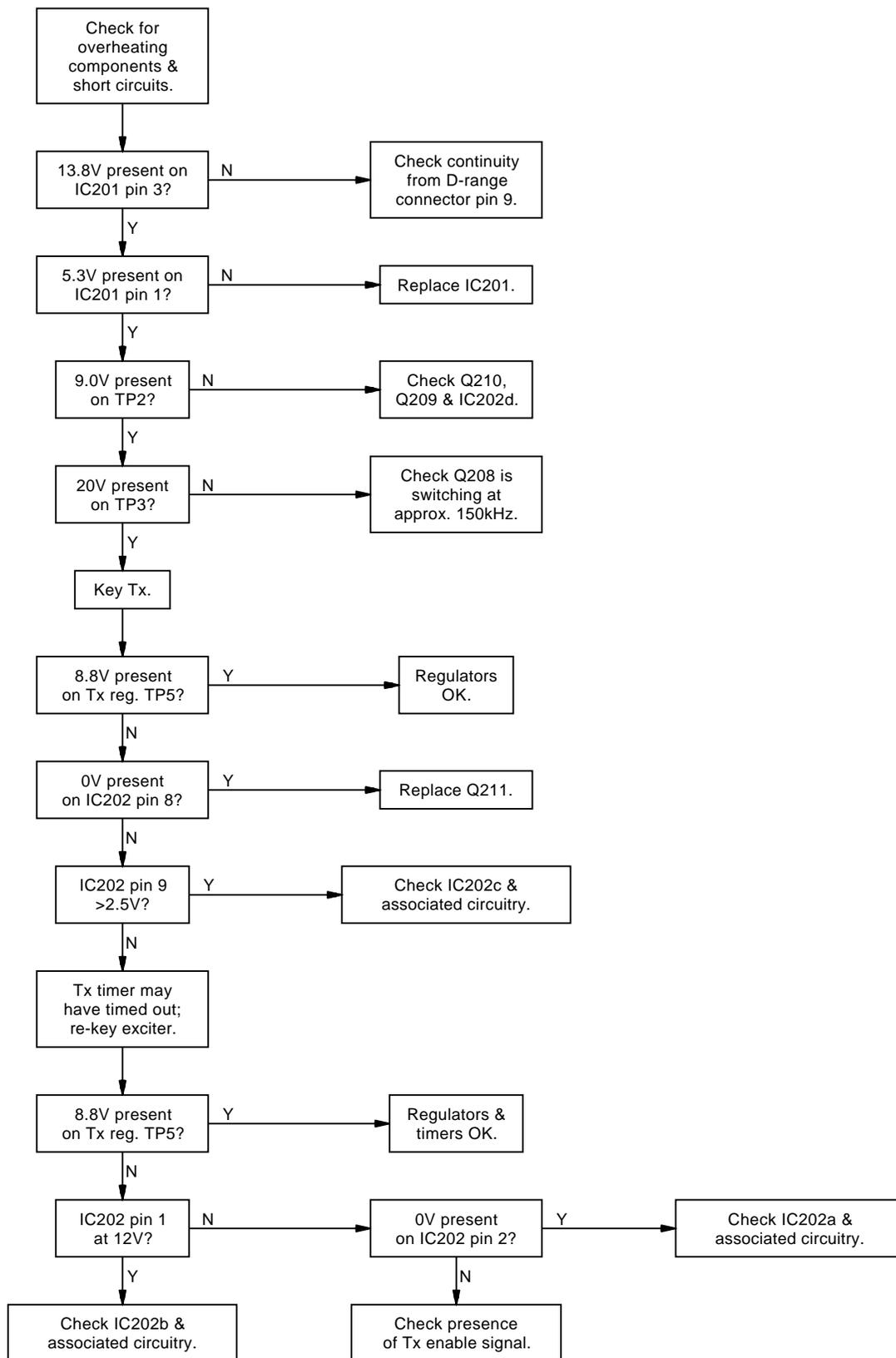
Set up the audio processor as described in Section 3.8.

Check that the demodulated RF output has the frequency response referred to in Section 4.6 with at least 5kHz [2.5kHz] deviation available at 1kHz modulating frequency.

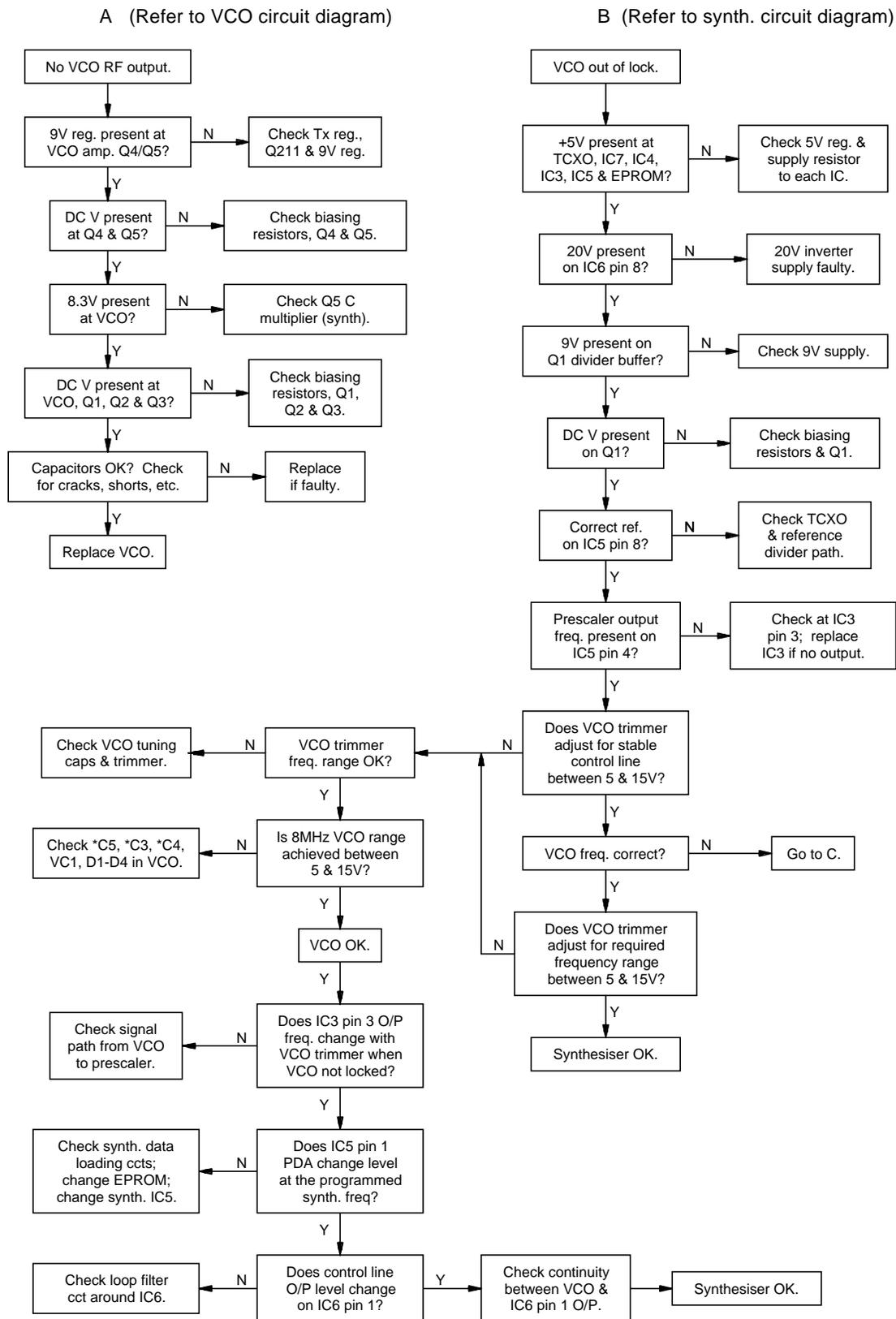
If the above result is not achieved, either the two modulators are incorrectly adjusted or a fault condition exists.

## 5.5 Fault Finding Charts

### 5.5.1 Regulator

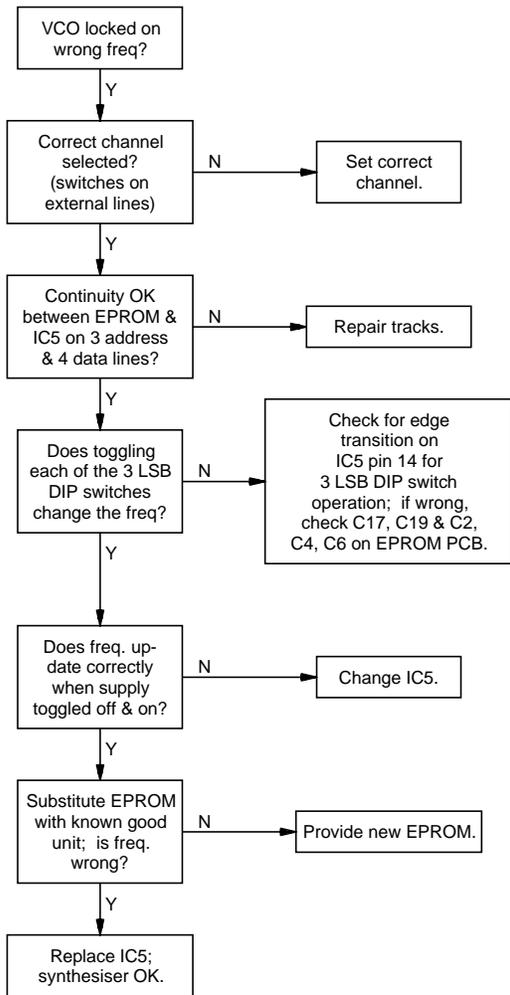


## 5.5.2 Synthesiser

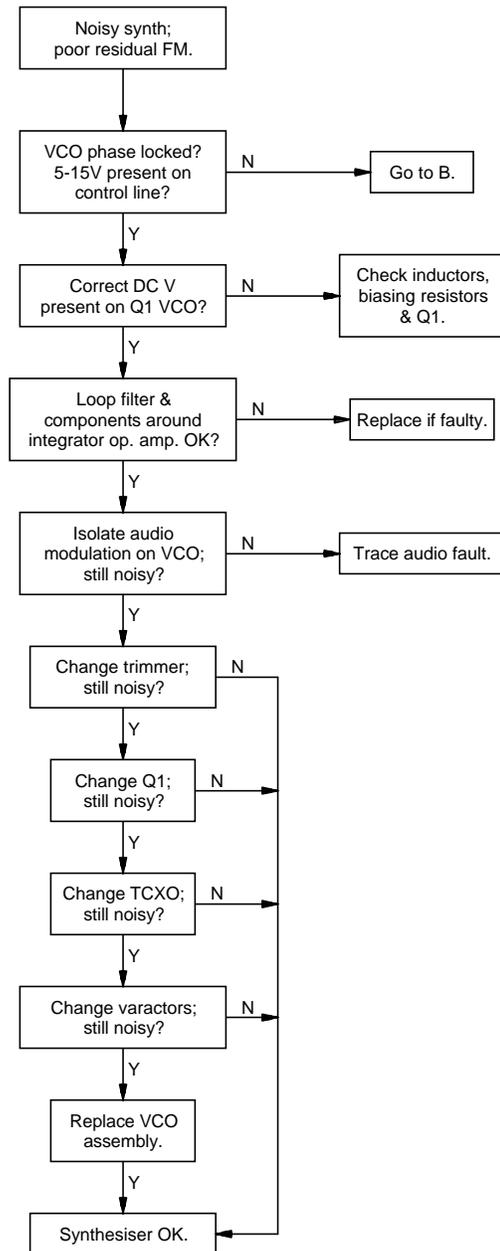


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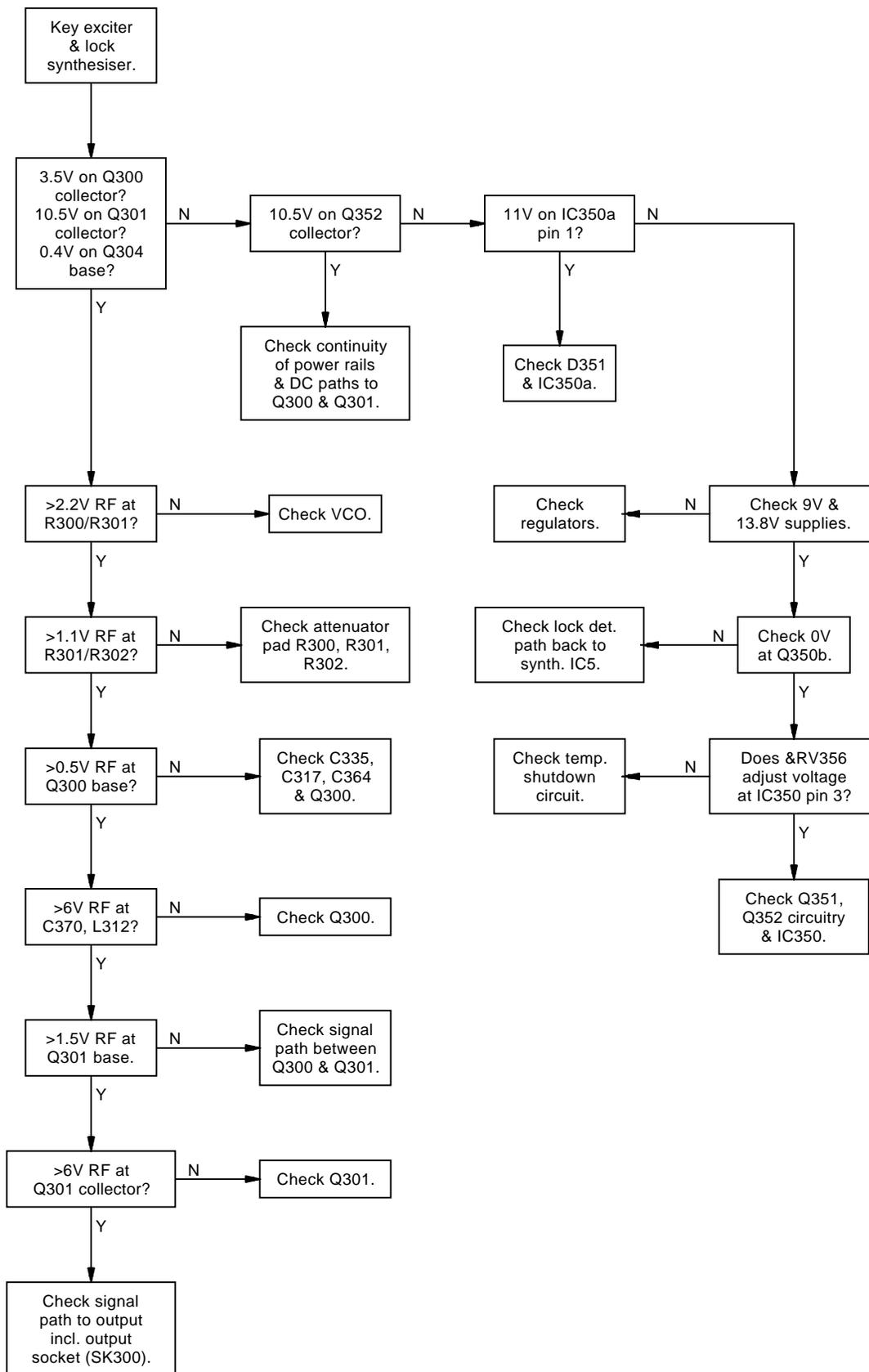
C (Refer to synth. circuit diagram)



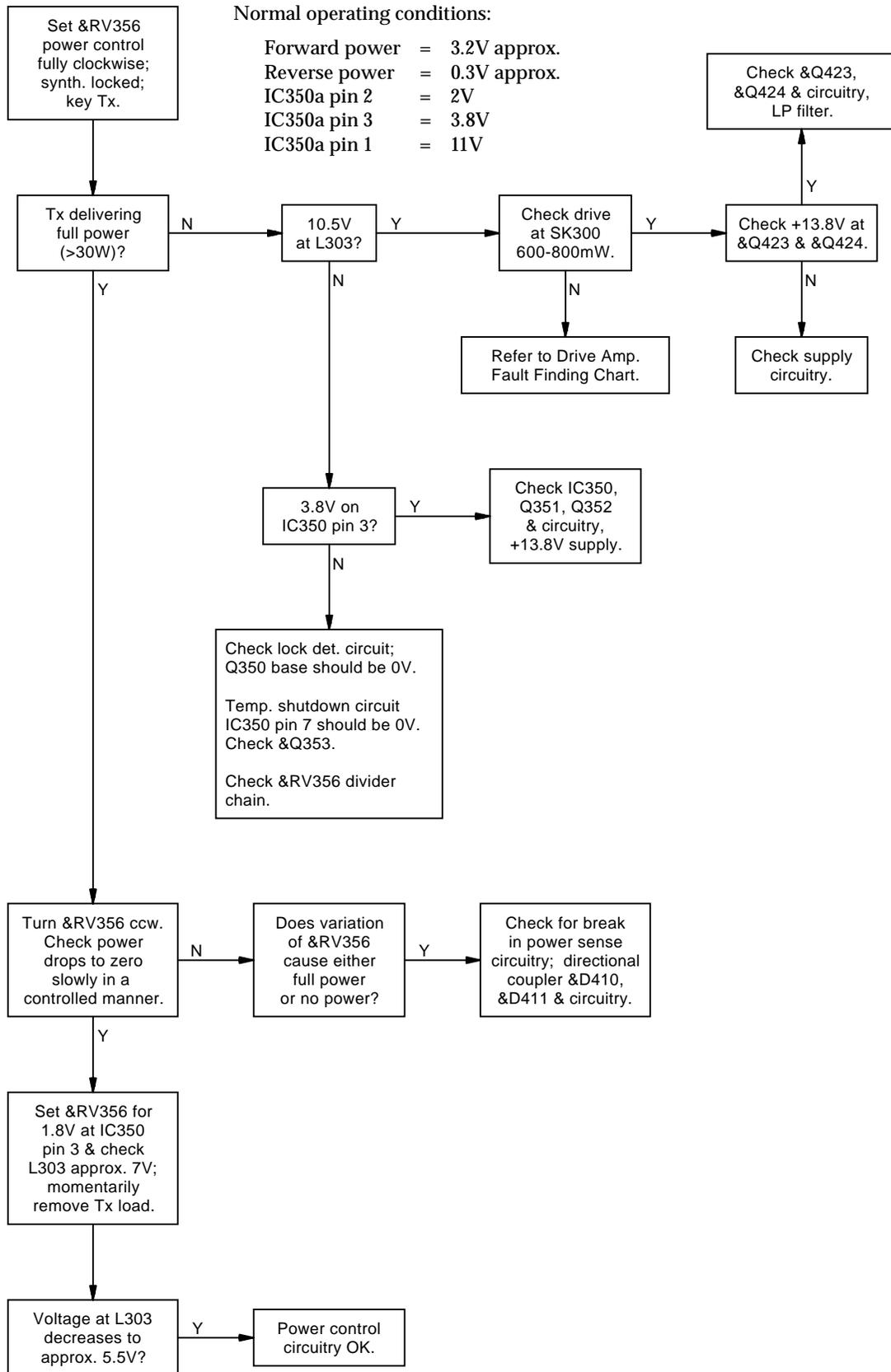
D (Refer to synth. & VCO circuit diagrams)



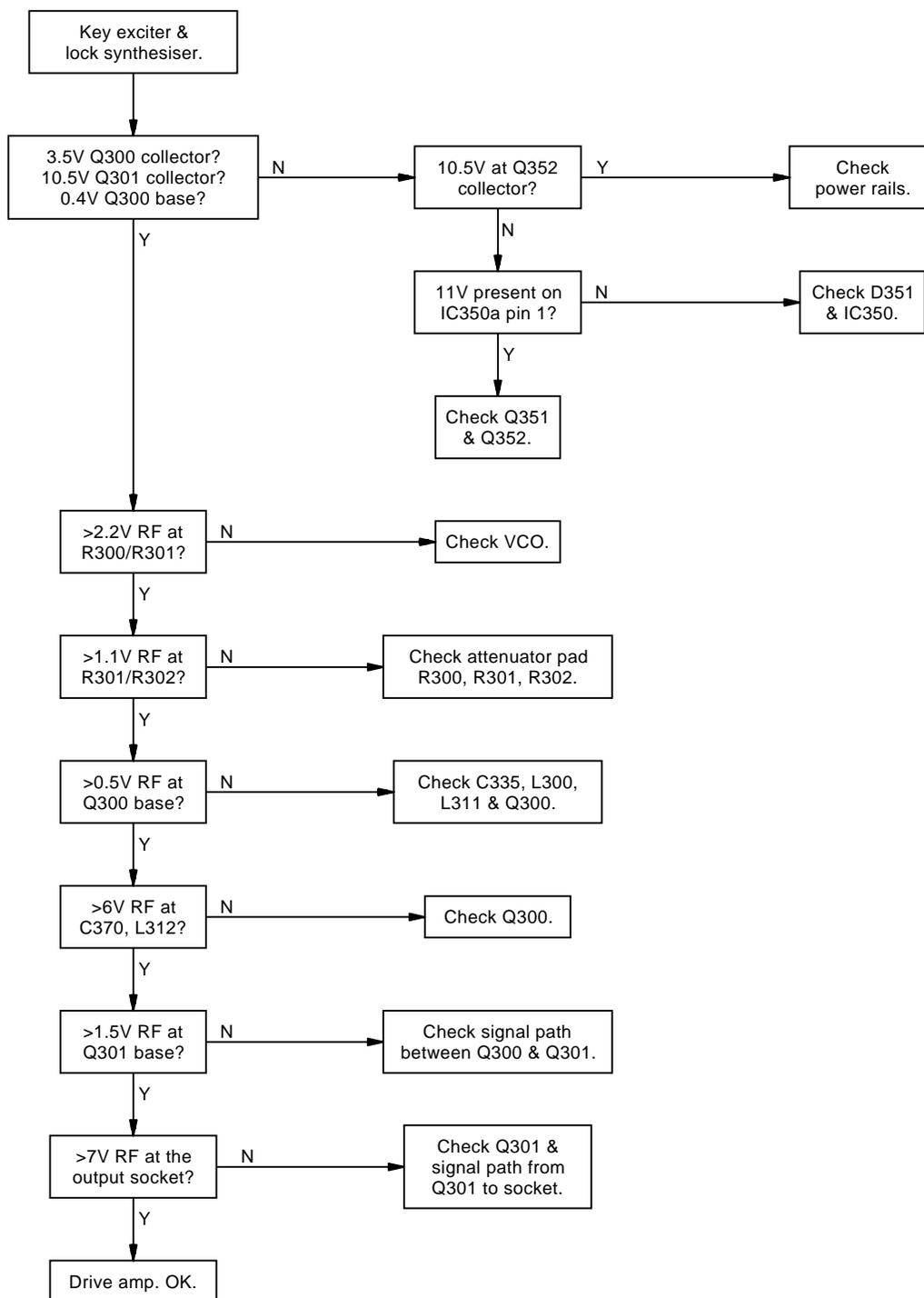
## 5.5.3 T836 Drive Amplifier



## 5.5.4 T836 PA & Power Control



## 5.5.5 T837 Exciter Drive Amplifier



## 5.5.6 Audio Processor

